Listing and Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently amended) A signaling apparatus, comprising:

data input means for receiving user input;

a memory having signaling data stored therein;

a processor, coupled to the memory and the data input means, for generating a signal sequence in response to a user input, the signal sequence comprising [[a]] 5 pulse position modulated (5PPM) [[signal]] signals, the last position of each said 5 PPM being always set to a low bit;

a modulator, coupled to the processor, for modulating the signal sequence onto a carrier signal;

a transmitter, coupled to the modulator, for transmitting the modulated signal sequence, including the 5 pulse position modulated [[signal]] signals.

2. Cancelled.

2 ~~3~~. (Original) The apparatus of claim 1, wherein said signal sequence is defined by a signaling protocol having a header portion and a payload portion.

3 ~~4~~. (Original) The apparatus of claim 2 ~~3~~, wherein said header portion comprises a plurality of fields for defining said payload portion.

4 ~~5~~. (Original) The apparatus of claim 2 ~~3~~, wherein said payload portion is a variable bit stream.

5 ~~6~~. (Original) The apparatus of claim 1, wherein said data input means comprises a keyboard.

6 ~~7~~. (Original) The apparatus of claim 1, wherein said signal sequence defines a position on a display of an image display device.

9 ~~8~~. (Original) In a control system having a signal transmitter for encoding and transmitting control signals and a signal receiver for receiving and decoding the control signals, the receiver comprising means for decoding a control signal in response to rising edges of pulses in the control signal, a control signal structure comprising:

a payload portion; and

a header portion, said header portion comprising a plurality of fields for defining said payload portion,

said header and payload portions being represented as a plurality of symbols encoded in accordance with a 5 pulse position modulation (5PPM) scheme, wherein each of said encoded symbols includes a last position that is always set to a low bit.

~~9~~ 8. (Original) The data structure of claim ~~8~~ 7, wherein said payload portion indicates a keyboard character.

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~~10~~ 9. (Original) The data structure of claim ~~8~~ 7, wherein said payload portion indicates coordinates for a pointing device.

~~11~~ 10. (Original) The apparatus of claim ~~8~~ 7, wherein said header portion comprises a repeat field for selectively transmitting only said header portion.

~~12~~ 11. (Original) In a control system having a signal transmitter for encoding and transmitting control signals and a signal receiver for receiving and decoding the control signals, the receiver comprising means for decoding a control signal in response to rising edge of pulses in the control signal, a method of providing control signals, comprising:

generating a signal sequence in response to a user input; and

converting said signal sequence into a plurality of symbols in accordance with a 5 pulse position modulated (5PPM) scheme,

wherein said converting step comprises setting a last position of each said 5 PPM symbol to a low bit.

12 ~~13~~. (Original) The method of claim 11 ~~12~~, further comprising the step of defining said symbol sequence by a signaling protocol having a header portion and a payload portion.

13 ~~14~~. (Original) The method of claim 11 ~~12~~, further comprising the step of sending bits representing a keyboard character as said payload portion.

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14 ~~15~~. (Original) The method of claim 11 ~~12~~, further comprising the step of sending bits representing a coordinate for a pointing device as said payload portion.